

## Meinberg Radio Clocks

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## GLN180PEX: Low Profile GPS / GLONASS Clock (PCI Express)

The board GLN180PEX is designed as a low profile board for computers with PCI Express interface. The rear slot cover integrates the SMA antenna connector, a BNC connector for modulated time codes, a 9pin D-SUB male connector and four status LEDs (2mm light pipes). With this standard height bracket you can use the D-SUB connector for I/O signals like RS-232 - PPS and PPM and you can use this interface for firmware updates.

The GLN180PEX will be delivered with a low profile bracket. You can mount this part instead of the standard bracket, to run the GLN180PEX in computers with smaller housing (e.g. 1U server).

The Monitoring Software allows the selection of the references (navigation systems)

\* GPS and GLONASS (standard)

\* GPS only

\* GLONASS only

### Important Note

This product is no longer available and may have been replaced by a newer product. We will, of course, continue to provide support for units that have already been purchased and are still in use. Please contact our [1][Sales Department](#) for further details.

This product has been discontinued and has been replaced with: [2]

### Key Features

- PCI Express Interface

- 2 time trigger inputs
- Programmable Pulse Outputs Frequency Synthesizer and Time Code Mode
- Memory Mapped I/O time reads for high access rates
- RS-232 interface
- IRIG-B/AFNOR time code outputs
- Plug and Play
- DCF77-simulation
- Allows direct transmission routes of up to 150 m when using Ultraflex H2010 coaxial cable
- Configurable time scale (UTC/local, GPS time, TAI)
- Driver software for all popular operating systems
- Possible usage in mobile applications
- Including 40 dB Multi GNSS L1 antenna, 20m pre-assembled Belden H155 cable and technical documentation on USB key

## Description

This PCI Express slot card is the best choice for adding a highly accurate time base to your servers or workstations. It can be used as a stratum 0 reference time source for NTP and transforms any machine into a Stratum 1 NTP server without consuming additional physical space in your server room.

The GLN180PEX comes with a truckload of features to enable software developers to overcome the timing limitations of COTS operating systems like Linux or Windows. The powerful and highly functional Meinberg API (Application Programming Interface) delivers an easy to use and portable way of accessing all Meinberg bus level timing devices, including ISA, PCI, PCI-X, PCI Express and USB time synchronization products.

Legacy interfaces like IRIG, 1PPS or serial time strings can be used to connect other equipment to the PCIe slot card and transfer the time base over dedicated cable connections to systems which cannot be synchronized via NTP or other network protocols.

The new Memory Mapped Access feature offers a fast, simple and efficient way of reading the current time with high precision.

The **Windows** driver package includes a time synchronization service which runs in the background and adjusts the Windows system time continuously and invisibly. This package also includes a monitor program to enable the user to check the status of the device and time adjustment service. If the monitor program is run with administrator rights, it can also be used to modify configurable parameters.

The **Linux** and **FreeBSD** driver packages include a kernel driver which allows the product to be used as a reference time source for the NTP daemon included in most Unix-like operating systems. This also allows the computer to be used as an NTP time server to provide accurate time to NTP clients on the network. Some command line tools can be used to modify configurable parameters and monitor the status of the clock in use.

Please contact Meinberg's Support Team for more information on using the card with other operating systems: [techsupport@meinberg.de](mailto:techsupport@meinberg.de).

The device's serial port is not required for operation but can be used to update the card's firmware, or to provide another computer with the current time via a serial time string.

## Satellite receiver for mobile applications:

The GLN timing board is designed for operating in mobile environments like cars, trains or airplanes. An optimized active GPS antenna RV-76 with 10 meters coax cable RG-58 for operation in mobile environment is still available.

## Characteristics

<b>Receiver Type</b>	<b>Combined GPS / GLONASS receiver</b> <ul style="list-style-type: none"><li>* Number of channels: 32</li><li>* Frequency band: GPS L1, GLONASS L1</li><li>* Standard Precision (GLONASS)</li><li>* Codes: C/A code (GPS)</li></ul>
<b>Navigation Features</b>	The satellite receiver can determine its position under the following conditions: <ul style="list-style-type: none"><li>* an acceleration of up to 5G</li><li>* a speed of up to 500 m/s</li><li>* an altitude of up to 18,000 meters</li></ul>
<b>Status Indicators</b>	<b>Status info by 4 LED light indicators (2mm light pipes)</b> <ul style="list-style-type: none"><li>* Init - blue: while the receiver passes through the initialization phase</li><li>* Nav Solved - green: positioning successfully</li><li>* Ant Fail - red: antenna faulty or not connected</li><li>* Fail - red: time is not synchronized</li></ul>
<b>Type of Antenna</b>	<b>40 dB GPS L1/GLONASS L1</b> Antenna with Integrated Lightning Protection <ul style="list-style-type: none"><li>* Frequency Band: 1575.42 ± 10 MHz / 1602-1615 MHz</li><li>* Antenna Gain:</li></ul>

<b>Synchronization Time</b>	Max. 1 minute in normal operating conditions Max. 25 minutes (average 12 minutes) upon first initialization or in the absence of saved satellite data
<b>Frequency Outputs</b>	Frequency output 10 MHz, TTL level
<b>Pulse Outputs</b>	3 Programmable TTL outputs, per default configured as: Channel 0: Pulse per second (TTL, RS232 level), pulse duration: 200 msec Channel 1: Pulse per minute (TTL), pulse duration: 200 msec Channel 2: DCF77 compatible pulses (TTL level), pulse width: 100/200 msec
<b>Accuracy of Pulse Outputs</b>	Depends on oscillator option: Standard: TCXO
<b>Interface</b>	Single serial RS-232 interface
<b>Serial Time String Output</b>	Baudrate: 300, 600, 1200, 2400, 4800, 9600, 19200 Baud Data format: 7N2, 7E1, 7E2, 8E1, 8N1, 8N2 Time telegram: [3] <a href="#">Meinberg Standard-Telegram</a> , SAT, Uni Erlangen (NTP), SPA, NMEA0183 (RMC) or [4] <a href="#">capture-telegramm</a>
<b>Statusbyte</b>	Informations about free running mode, daylight savings time and DST pre-switch announcement, synchronization since last reset, GMT/UTC time and validity of the hardware clock data
<b>PWM Time Code Output</b>	DCLS, TTL into 50 ohm (active high or active low)
<b>AM Time Code Output</b>	IRIG AM sine wave signal: 3Vpp (MARK), 1Vpp (SPACE) into 50 ohm
<b>Supported Timecode Formats</b>	<b>IRIG B002:</b> 100pps, DCLS signal, no carrier, BCD time-of-year <b>IRIG B122:</b> 100pps, AM sine wave signal, 1 kHz carrier, BCD time-of-year <b>IRIG B003:</b> 100pps, DCLS signal, no carrier, BCD time-of-year, SBS time-of-day <b>IRIG B123:</b> 100pps, AM sine wave signal, 1kHz carrier, BCD time-of-year, SBS time-of-day <b>IRIG B006:</b> 100 pps, DCLS Signal, no carrier, BCD time-of-year, year <b>IRIG B126:</b> 100 pps, AM sine wave signal, 1 kHz carrier frequency, BCD time-of-year, Year <b>IRIG B007:</b> 100 pps, DCLS Signal, no carrier, BCD time-of-year, year, SBS time-of-day <b>IRIG B127:</b> 100 pps, AM sine wave signal, 1 kHz carrier frequency, BCD time-of-year, year, SBS time-of-day <b>IEEE1344:</b> Code according to IEEE1344-1995, 100pps, AM sine-wave signal, 1kHz carrier, BCD time-of-year, SBS time-of-day, IEEE1344 expansion for date, time zone, daylight saving and leap second in Control Functions segment <b>C37.118:</b> Like IEEE1344 - with inverted sign bit for UTC offset <b>AFNOR:</b> Code according to NFS-87500, 100pps, AM sine-wave signal, 1kHz carrier, BCD time-of-year, complete date, SBS time-of-day
<b>Time-Trigger inputs</b>	Resolution: 100 nsec, triggered by falling TTL slope Time of trigger event readable via computer slot or optional second RS232-interface

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<b>Electrical Connectors</b>	SMA connector for antenna BNC female connector for modulated timecode 9 pin sub D male connector (with standard bracket)
<b>Computer interface</b>	Single lane (x1) PCI Express (PCIe) Interface PCI Express r1.0a compatible
<b>Backup Battery Type</b>	When main power supply fails, hardware clock runs free on quartz basis, almanac data is stored in RAM Life time of lithium battery min. 10 years
<b>Board type</b>	Low Profile card (68,90 x 150 mm)
<b>Supported Temperature</b>	Operational: 0 - 50 °C (32 - 122 °F) Storage: -20 - 70 °C (-4 - 158 °F)
<b>Supported Humidity</b>	Max. 85 % (non-condensing) at 40 °C
<b>Warranty</b>	Three-year warranty
<b>Options</b>	<b>Oscillator upgrade:</b>  * OCXO-LQ, -MQ or -HQ (instead of TCXO) for extended Holdover capabilities (see [5] <a href="#">oscillator table</a> for further details)
<b>RoHS Status of Product</b>	This product is fully RoHS-compliant.
<b>WEEE Status of Product</b>	This product is handled as a B2B (Business to Business) category product. To ensure that the product is disposed of in a WEEE-compliant fashion, it can be returned to the manufacturer. Any transportation expenses for returning this product (at end-of-life) must be covered by the end user, while Meinberg will bear the costs for the waste disposal itself.

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## Manual

The English manual is available as a PDF file: [6][Download \(PDF\)](#)

## Links:

[1] <mailto:sales@meinberg.de>

[2] <https://www.meinbergglobal.com/english/products/gns181pex.htm>

[3] <https://www.meinbergglobal.com/english/specs/timestr.htm>

[4] <https://www.meinbergglobal.com/english/specs/capstr.htm>

[5] <https://www.meinbergglobal.com/english/specs/gpsopt.htm>

[6] <https://www.meinbergglobal.com/download/docs/manuals/english/gln180pex.pdf>